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1	APPLICATION	FILING or	GRP ART				
	NUMBER	371(c) DATE	UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	TOT CLAIMS	IND CLAIMS
	10/595.762	07/13/2006	2652	1030	SC12991CF	10	2.

**CONFIRMATION NO. 6202** 

23125
FREESCALE SEMICONDUCTOR, INC.
LAW DEPARTMENT
7700 WEST PARMER LANE MD:TX32/PL02

FILING RECEIPT

Date Mailed: 04/07/2008

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections

Applicant(s)

Kyle Patterson, Froges, FRANCE; Kirk Strozewski, Round Rock, TX;

**Assignment For Published Patent Application** 

Freescales Semiconductor, Inc., Austin, TX

Power of Attorney: The patent practitioners associated with Customer Number 23125

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/EP05/01511 02/15/2005

Foreign Applications

EUROPEAN PATENT OFFICE (EPO) 04290429.2 02/17/2004

If Required, Foreign Filing License Granted: 03/21/2008

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US 10/595,762** 

**Projected Publication Date: 07/03/2008** 

Non-Publication Request: No

Early Publication Request: No

#### Title

Immersion Lithography Technique And Product Using A Protection Layer Covering The Resist

### **Preliminary Class**

360

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Title 37, Code of Federal Regulations, 5.11 & 5.15

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